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OpenMP on Ranger and Stampede (with Labs)

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Parallel Computing at TACC: Ranger to Stampede Transition November 6, 2012 Based on materials developed by Kent Milfeld at TACC



What is OpenMP?

- OpenMP is an acronym for Open Multi-Processing
- An Application Programming Interface (API) for developing parallel programs in shared-memory architectures
- Three primary components of the API are:
 - Compiler Directives
 - Runtime Library Routines
 - Environment Variables
- De facto standard -- specified for C, C++, and FORTRAN
- <u>http://www.openmp.org/</u> has the specification, examples, tutorials and documentation



LAB: OMP Hello World

Parallel Region: C/C++ and Fortran

```
1 #pragma omp parallel
2 { code block
3 a = work(...);
4 }
```

```
!$omp parallel
    code block
    call work(...)
!$omp end parallel
```

Line 1 Team of threads is formed at parallel region

- Lines 2–3 Each thread executes code block and subroutine call, no branching into or out of a parallel region
- Line 4 All threads synchronize at end of parallel region (implied barrier)



OpenMP = Multithreading

- All about executing concurrent work (tasks)
 - Tasks execute independently
 - Tasks access the same shared memory
 - Shared variable updates must be mutually exclusive
 - Synchronization through barriers
- Simple way to do multithreading run tasks on multiple cores/units
- Insert parallel directives to run tasks on concurrent threads

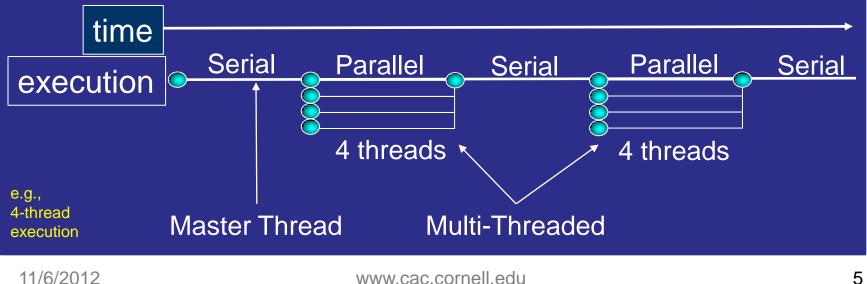
```
// repetitive work
#pragma omp parallel for
for (i=0; i<N; i++)
        a[i] = b[i] + c[i];</pre>
```

```
// repetitive updates
#pragma omp parallel for
for (i=0; i<N; i++)
    sum = sum + b[i]*c[i];</pre>
```



OpenMP Fork-Join Parallelism

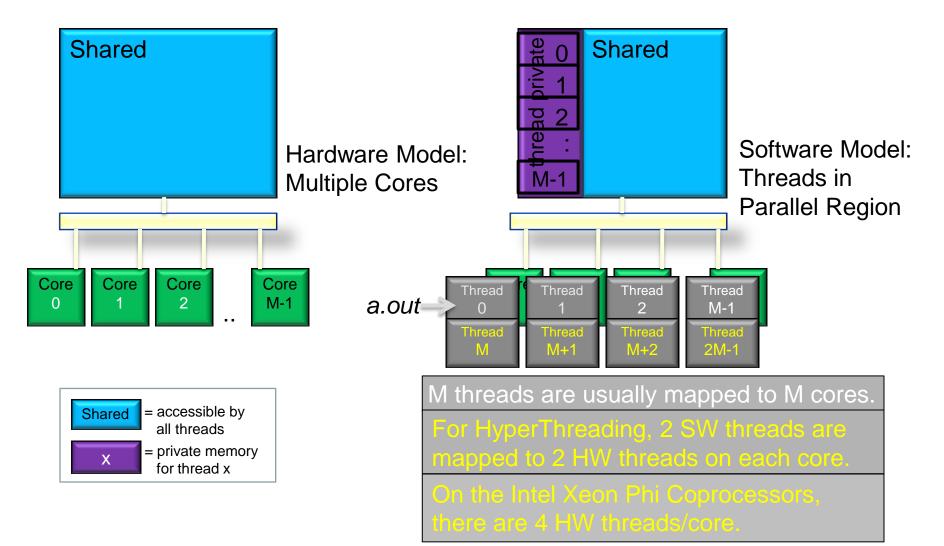
- Programs begin as a single process: master thread
- Master thread executes until a parallel region is encountered
 - Master thread creates (forks) a team of parallel threads —
 - Threads in team simultaneously execute tasks in the parallel region _
 - Team threads synchronize and terminate (join); master continues —





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OpenMP on Shared Memory Systems





Thread Memory Access

- Every thread has access to "global" (shared) memory
 - All threads share the same address space
 - Threads don't communicate like MPI processes
- But need to avoid race conditions with shared memory. Examples:
 - 1. If multiple writers are going in no particular order, last writer "wins"
 - 2. A reader may either precede or follow a writer lack of synchronization
 - 3. Threads may overlap in a code block, causing conditions 1 and 2
- What do you with a race condition?
 - Don't introduce one in the first place: it's a bug, hard to debug
 - Impose order with barriers (explicit/implicit synchronization)
- Use mutual exclusion (mutex) directives to protect critical sections, where one thread must run at a time (at a performance penalty)



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Example of a Critical Section

Thread 0		Thread 1		Value
read	←			0
increment				0
write	\rightarrow			1
		read	←	1
		increment		1
		write	\rightarrow	2

Intended

Thread 0		Thread 1		Value
				0
read	←			0
increment		read	←	0
write	\rightarrow	increment		1
		write	\rightarrow	1
				1

Possible...

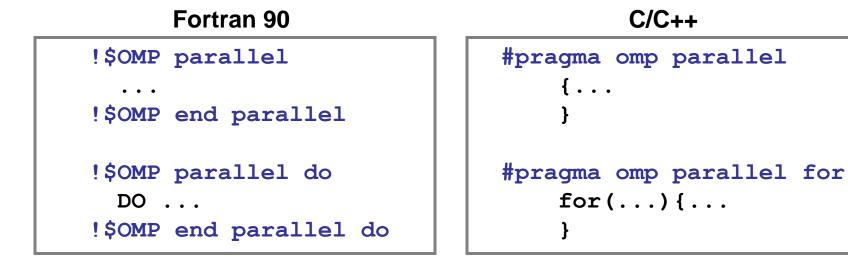
- In a critical section, need *mutual exclusion* to get intended result
- The following OpenMP directives prevent this race condition:

#pragma omp critical - for a code block (C/C++)
#pragma omp atomic - for single statements



OpenMP Directives

- OpenMP directives are comments in source code that specify parallelism for shared-memory parallel (SMP) machines
- FORTRAN compiler directives begin with one of the sentinels
 !\$OMP, C\$OMP, or *\$OMP use !\$OMP for free-format F90
- C/C++ compiler directives begin with the sentinel **#pragma omp**





Role of the Compiler

- OpenMP relies on the compiler to do the multithreading
 - Compiler recognizes OpenMP directives, builds in appropriate code
- A special flag is generally required to enable OpenMP
 - GNU: gcc -fopenmp
 - Intel: icc -openmp
- Additional flags are required to enable MIC instructions
 - Offload marked sections to MIC: icc -offload-build -openmp
 - Build whole code native to MIC: icc -mmic [-openmp]
 - These options are valid for Intel compilers only
 - Details could change when "Xeon Phi" (the MIC product) is released



OpenMP Syntax

- OpenMP Directives: Sentinel, construct, and clauses
 #pragma omp construct [clause [[,]clause]...]
 C
 !\$omp construct [clause [[,]clause]...]
 F90
- Example

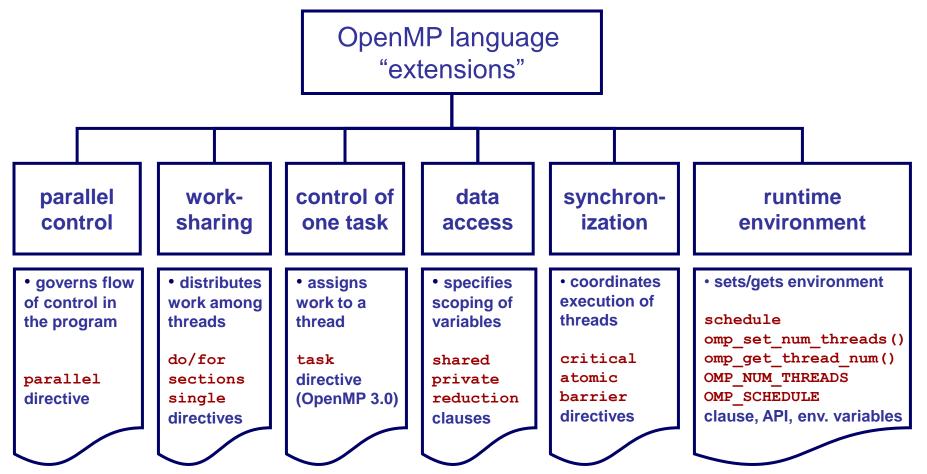
#pragma omp parallel private(i) reduction(+:sum)C!\$ompparallel private(i) reduction(+:sum)F90

 Most OpenMP constructs apply to a "structured block", that is, a block of one or more statements with one point of entry at the top and one point of exit at the bottom.



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OpenMP Constructs



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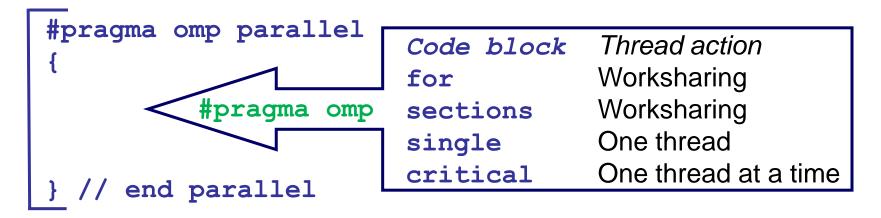
OpenMP Parallel Directives

PARALLEL Replicated – executed by all threads {code1} Worksharing – divided among threads DO do I = 1, N*4PARALLEL DO {code2} do I = 1, N*4end do {code} PARALLEL {code3} end do {code} END PARALLEL END PARALLEL DO END PARALLEL code1 code1 code1 code1 I=N+1,2N I=2N+1,3N I=3N+1,4N I=1.N I=1.N I=N+1,2N I=2N+1,3N I=3N+1.4N code code code code code code2 code code code2 code2 code code2 code3 code3 code3 code3 Replicated Worksharing Combined



OpenMP Worksharing, Mutual Exclusion

Use OpenMP directives to specify worksharing in a parallel region, as well as mutual exclusion



parallel do/for
parallel sections

Directives can be combined, if a parallel region has just one worksharing construct.



Worksharing Loop: C/C++

1 #pragma omp parallel for 2 for (i=0; i<N; i++) 3 { 4 a[i] = b[i] + c[i]; 5 } 6

General form:

```
#pragma omp parallel
{
    #pragma omp for
    for (i=0; i<N; i++)
    {a[i] = b[i] + c[i];}
}</pre>
```

Line 1 Team of threads formed (parallel region).

Lines 2–6 Loop iterations are split among threads. Implied barrier at end of block(s) {}.

Each loop iteration must be independent of other iterations.



Worksharing Loop: Fortran

General form:

```
1 !$omp parallel do
2 do i=1,N
3 a(i) = b(i) + c(i)
4 enddo
5 !$omp end parallel do
6
```

```
!$omp parallel
!$omp do
  do i=1,N
   a(i) = b(i) + c(i)
enddo
!$omp end parallel
```

Line 1 Team of threads formed (parallel region).

- Lines 2–5 Loop iterations are split among threads.
- Line 5 (Optional) end of parallel loop (implied barrier at enddo).

Each loop iteration must be independent of other iterations.

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OpenMP Clauses

- Directives dictate what the OpenMP thread team will do
- Examples:
 - Parallel regions are marked by the **parallel** directive
 - Worksharing loops are marked by **do**, **for** directives (Fortran, C/C++)
- *Clauses* control the behavior of any particular OpenMP directive
- Examples:
 - 1. Scoping of variables: private, shared, default
 - 2. Initialization of variables: copyin, firstprivate
 - 3. Scheduling: static, dynamic, guided
 - 4. Conditional application: **if**
 - 5. Number of threads in team: num_threads



LAB: Worksharing Loop

Private, Shared Clauses

- In the following loop, each thread needs a private copy of temp
 - The result would be unpredictable if temp were shared, because each processor would be writing and reading to/from the same location

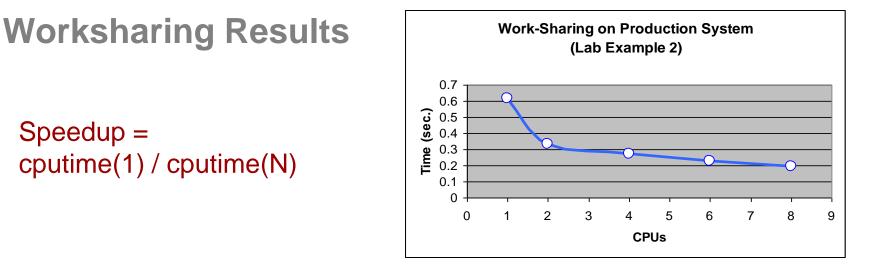
```
!$omp parallel do private(temp,i) shared(A,B,C)
    do i=1,N
        temp = A(i)/B(i)
        C(i) = temp + cos(temp)
        enddo
!$omp end parallel do
```

- A "lastprivate(temp)" clause will copy the last loop (stack) value of temp to the (global) temp storage when the parallel DO is complete
- A "firstprivate(temp)" initializes each thread's temp to the global value



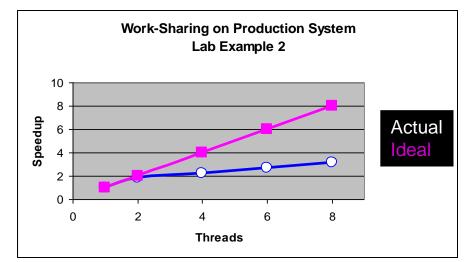
Speedup =

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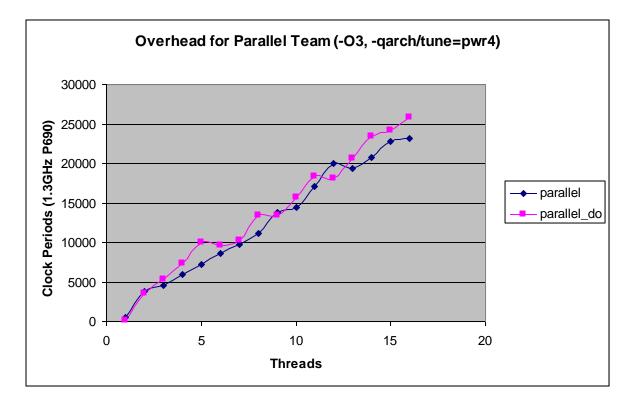
If work is completely parallel, scaling is linear.

Scheduling, memory contention and overhead can impact speedup and Mflop/s rate.





Overhead to Fork a Thread Team

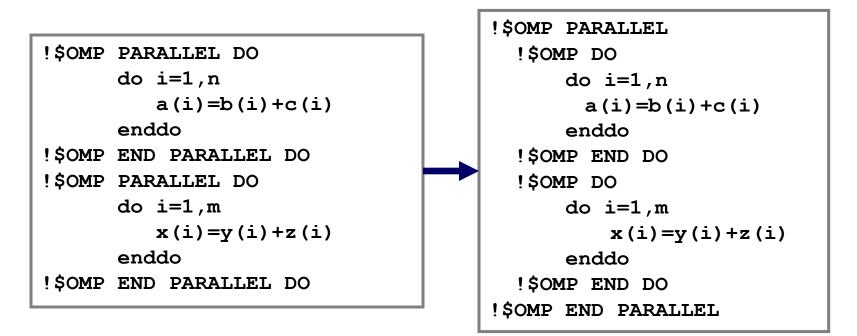


• Increases roughly linearly with number of threads



Merging Parallel Regions

The !\$OMP PARALLEL directive declares an entire region as parallel; therefore, merging work-sharing constructs into a single parallel region eliminates the overhead of separate team formations





LAB: OMP Functions

Runtime Library Functions

<pre>omp_get_num_threads()</pre>	Number of threads in current team
<pre>omp_get_thread_num()</pre>	Thread ID, {0: N-1}
<pre>omp_get_max_threads()</pre>	Number of threads in environment, OMP_NUM_THREADS
<pre>omp_get_num_procs()</pre>	Number of machine CPUs
<pre>omp_in_parallel()</pre>	True if in parallel region & multiple threads executing
<pre>omp_set_num_threads(#)</pre>	Changes number of threads for parallel region, if dynamic threading is enabled



Environment Variables, More Functions

• To control the OpenMP runtime environment

OMP_NUM_THREADS	Set to permitted number of threads: this is the value returned by omp_get_max_threads()
OMP_DYNAMIC	TRUE/FALSE for enable/disable dynamic threading (can also use the function below)

• To enable dynamic thread count (*not* dynamic scheduling!)

<pre>omp_set_dynamic()</pre>	Set state of dynamic threading: if equal to "true", <pre>omp_set_num_threads()</pre> controls thread count
<pre>omp_get_dynamic()</pre>	True if dynamic threading is on



Additional Topics to Explore...

- Schedule clause: specify how to divide work among threads schedule(static) schedule(dynamic,M)
- Reduction clause: perform collective operations on shared variables
 reduction (+: asum) reduction (*: aprod)
- Nowait clause: remove the barrier at the end of a parallel section
 for ... nowait
 end do nowait
- Lock routines: make mutual exclusion more lightweight and flexible
 omp_init_lock(var)
 omp_set_lock(var)



Some Programming Models for Intel MIC

- Intel Threading Building Blocks (TBB)
 - For C++ programmers
- Intel Cilk Plus
 - Task-oriented add-ons for OpenMP
 - Currently for C++ programmers, may become available for Fortran
- Intel Math Kernel Library (MKL)
 - Automatic offloading by compiler for some MKL features
 - MKL is inherently parallelized with OpenMP
- OpenMP
 - On Stampede, TACC expects that this will be the most interesting programming model for HPC users



MIC Programming with OpenMP

- In OpenMP 4.0, accelerator syntax may ultimately be standardized, but for now, we use special MIC directives for the Intel compilers
- OpenMP pragma is preceded by MIC-specific pragma
 - Fortran: !dir\$ omp offload target(mic) <...>
 - C: #pragma offload target(mic) <...>
- All data transfer is handled by the compiler
 - User control provided through optional keywords
- I/O can be done from within offloaded region
 - Data can "stream" through the MIC; no need to leave MIC to fetch new data
 - Also very helpful when debugging (print statements)
- Specific subroutines can be offloaded, including MKL subroutines



Example 1

2-D array (**a**) is filled with data on the coprocessor

Data management done <u>automatically</u> by compiler

- Memory is allocated on coprocessor for (a)
- Private variables
 (i,j,x) are created
- Result is copied back

```
use omp lib
                                 ! OpenMP
       :: n = 1024
                                 ' Size
integer
real, dimension(:,:), allocatable :: a ! Array
integer
                :: i, j
                                 ! Index
real
               :: x
                                ! Scalar
allocate(a(n,n))
                             ! Allocation
!$omp parallel do shared(a,n), & ! Par. region
 private(x, i, j), schedule(dynamic)
do j=1, n
 do i=j, n
   x = real(i + j); a(i,j) = x
```



Example 2

I/O from offloaded region:

- File is opened and closed by one thread (omp single)
- All threads take turns reading from the file (omp critical)

Threads may also read in parallel (not shown)

- Parallel file system
- Threads read parts from different targets

```
#pragma offload target(mic) //Offload region
#pragma omp parallel
  #pragma omp single /* Open File */
 printf("Opening file in offload region\n");
  f1 = fopen("/var/tmp/mydata/list.dat","r");
  #pragma omp for
  for(i=1;i<n;i++) {</pre>
    #pragma omp critical
    { fscanf(f1,"%f",&a[i]);}
    a[i] = sqrt(a[i]);
  #pragma omp single
 printf("Closing file in offload region\n");
  fclose (f1);
```



Example 3

Two routines, MKL's sgemm and my sgemm

- Both are called with offload directive
- my_sgemm specifies explicit in and out data movement

Use attributes to

have routine compiled for the coprocessor, or link coprocessor-based MKL

LAB: Hand-coding vs. MKL

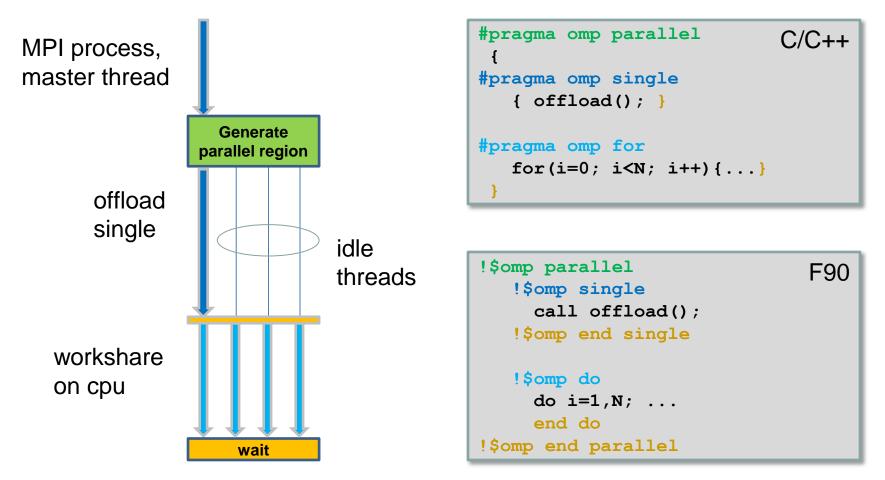
```
! snippet from the caller...
! offload MKL routine to accelerator
!dir$ attributes offload:mic :: sgemm
!dir$ offload target(mic)
call & sgemm('N','N',n,n,n,alpha,a,n,b,n,beta,c,n)
! offload hand-coded routine with data clauses
!dir$ offload target(mic) in(a,b) out(d)
call my_sgemm(d,a,b)
```

```
! snippet from the hand-coded subprogram...
!dir$ attributes offload:mic :: my_sgemm
subroutine my_sgemm(d,a,b)
real, dimension(:,:) :: a, b, d
!$omp parallel do
do j=1, n
    do i=1, n
    d(i,j) = 0.0
    do k=1, n
        d(i,j) = d(i,j)+a(i,k)*b(k,j)
        enddo; enddo
end subroutine
```



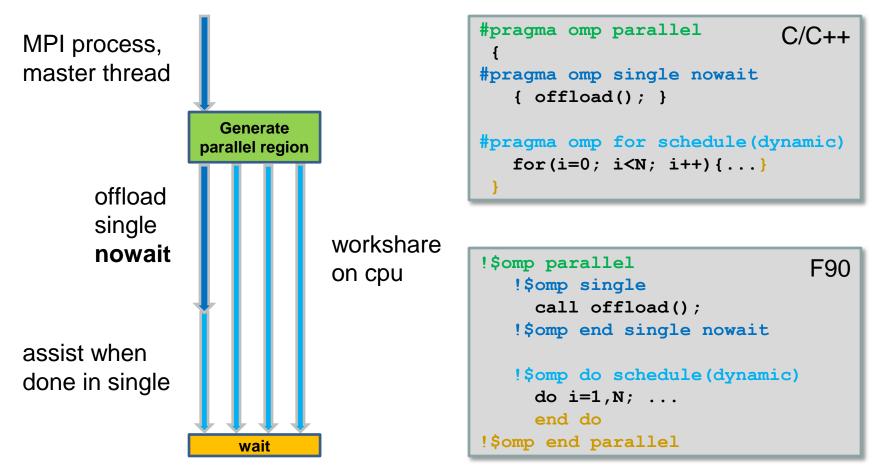
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Heterogeneous Threading, Sequential





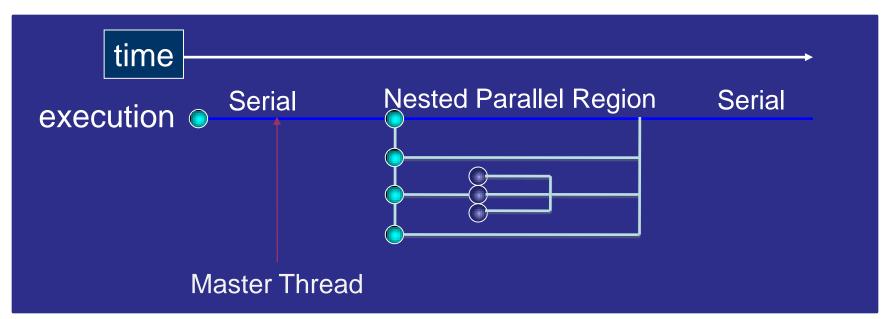
Heterogeneous Threading, Concurrent





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Loop Nesting



- OpenMP 3.0 supports nested parallelism, older implementations may ignore the nesting and serialize inner parallel regions.
- A nested parallel region can specify any number of threads to be used for the thread team, new id's are assigned.